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Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Place Block Diagram here (if appropriate) or delete this text box.
If using a block diagram from another tool, save the picture as a .bmp file.
Then, use menu Place|Drawing Tools|Graphic to insert the .png/.svg/.bmp file on the schematic.

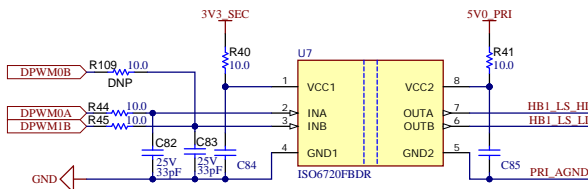
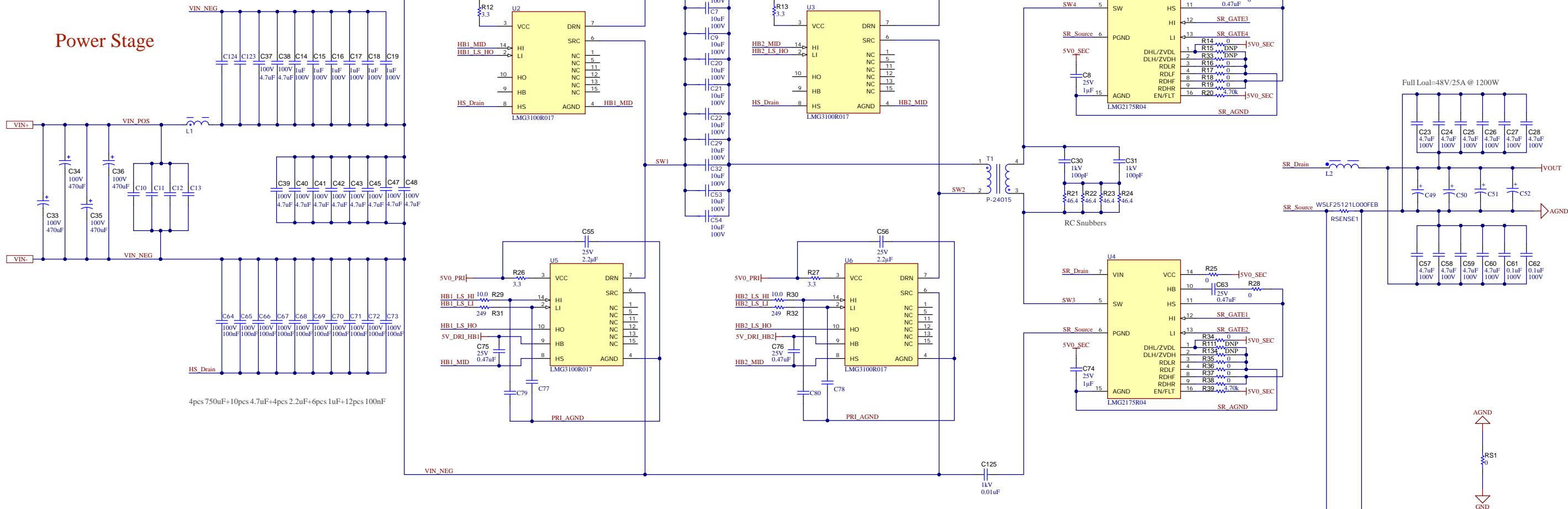
Orderable: ChangeMe in variant		Designed for: Public Release		Mod. Date: 12/2/2024	
TID #: N/A		Project Title: PMP41130			
Number: PMP41130		Rev: E1		Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001		Sheet: 1 of 5	
Drawn By:		File: PMP41130_SchDoc		Size: B	
Engineer: Xingqi (Eric) Chen		Contact: http://www.ti.com/support			

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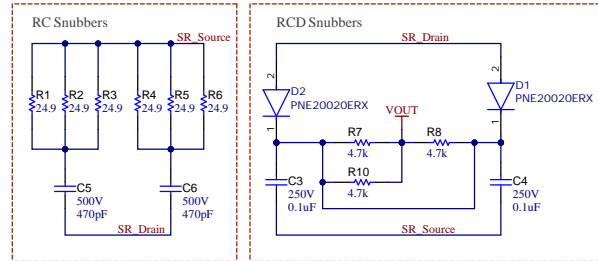
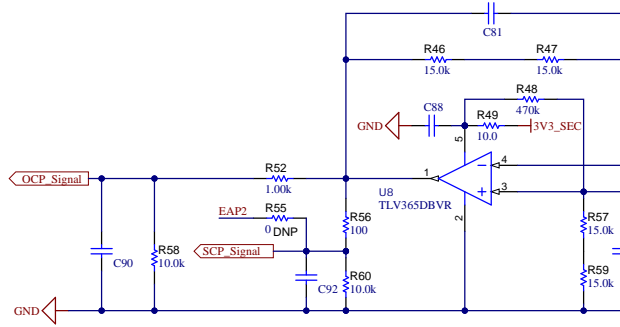
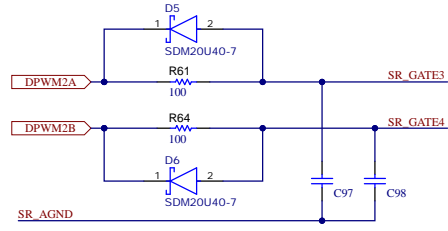
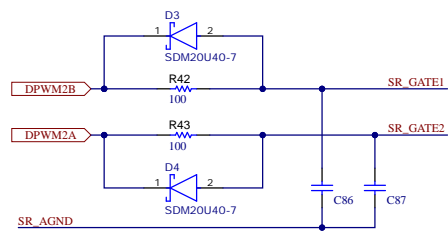
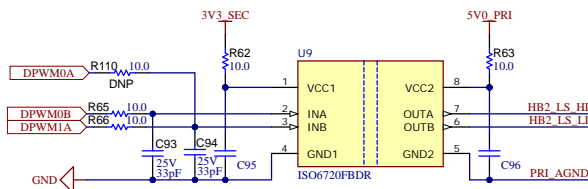
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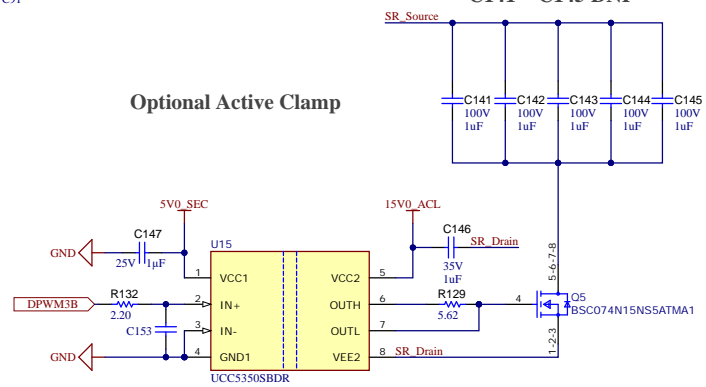
Power Stage

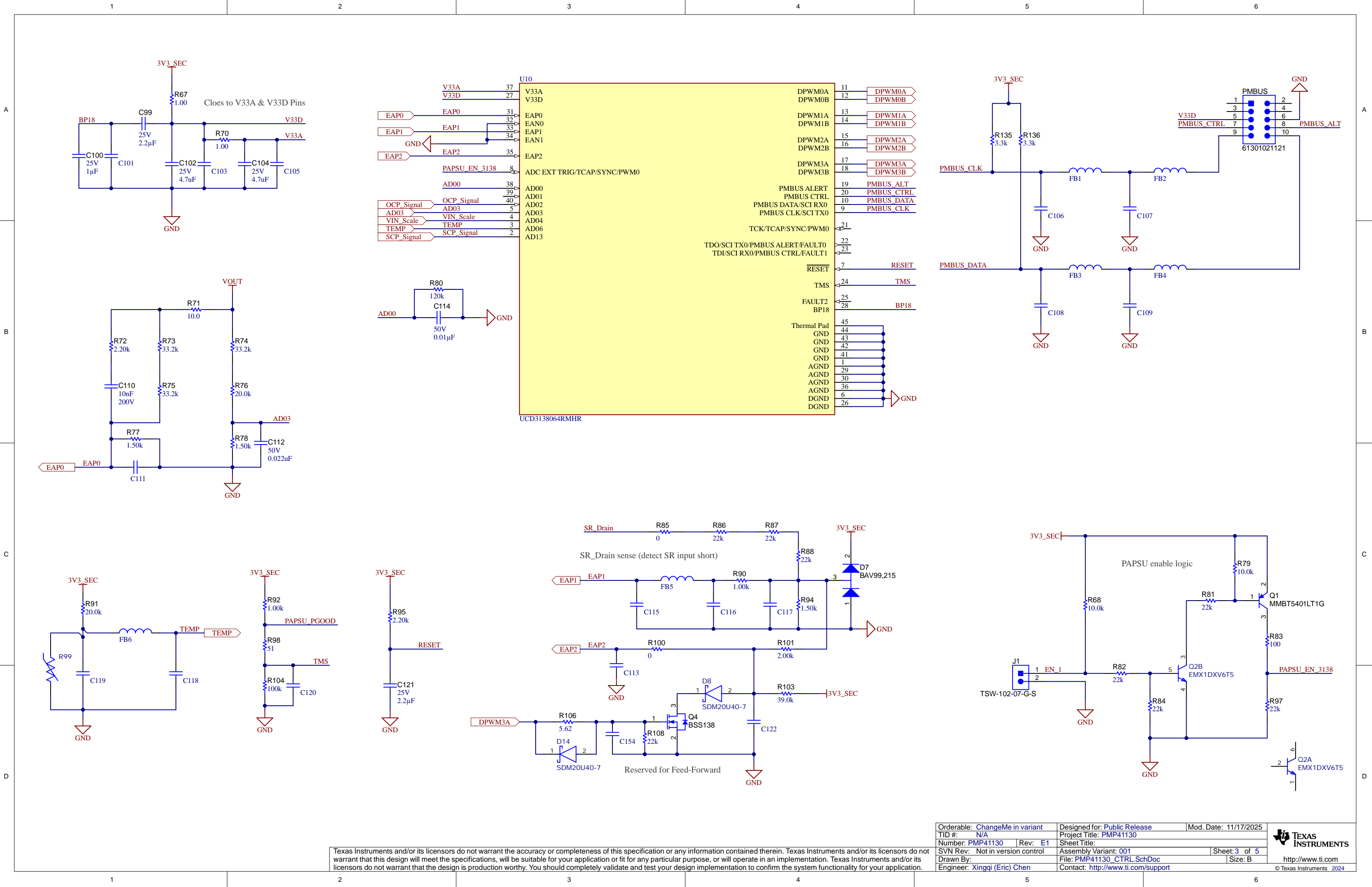


Change ISO6720BDR to ISO6720FBDR



Optional Active Clamp





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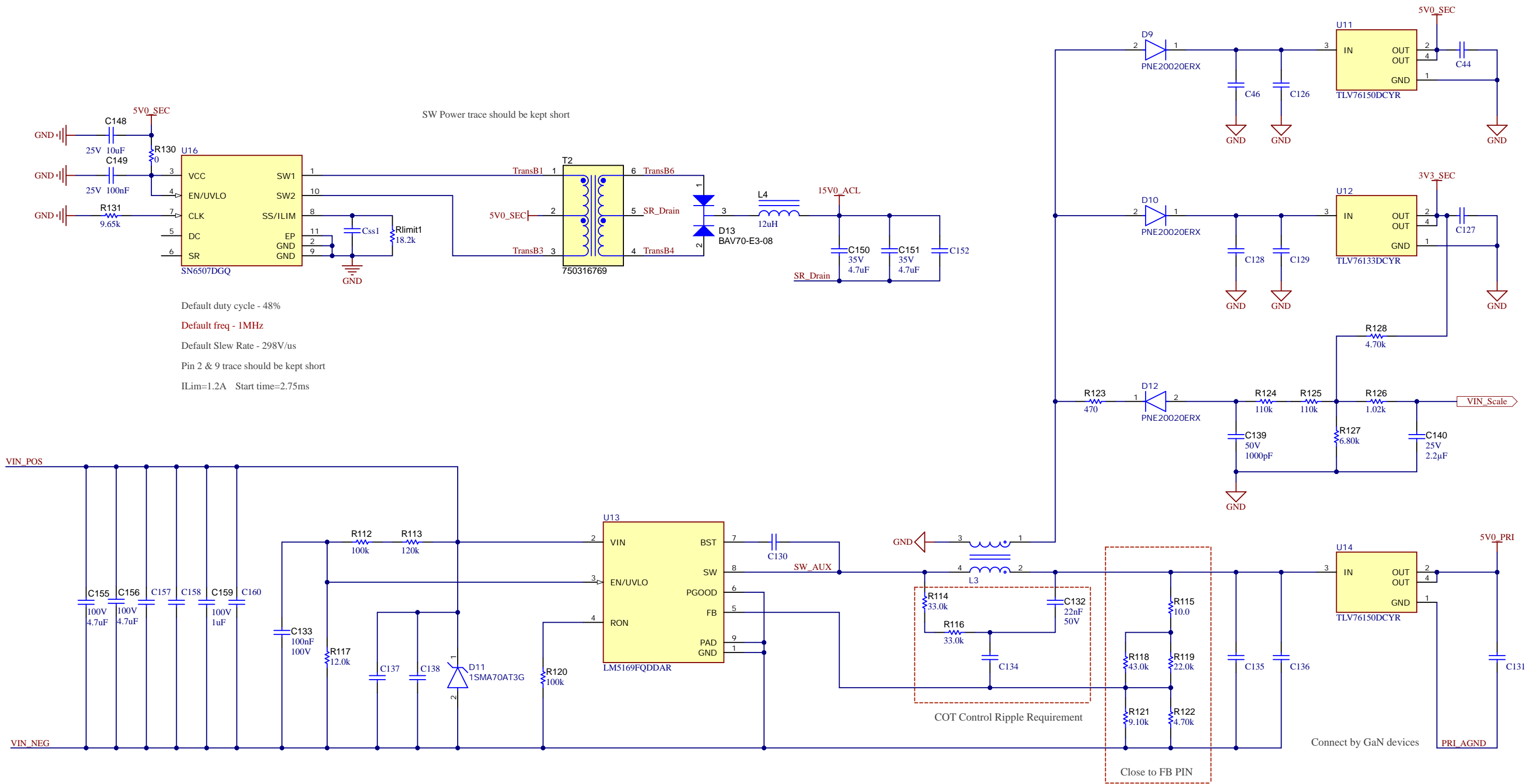
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Orderable: ChangeMe in variant		Designed for: Public Release		Mod. Date: 11/17/2025	
TID #: N/A		Project Title: PMP41130			
Number: PMP41130		Rev: E1		Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001		Sheet: 4 of 5	
Drawn By:		File: PMP41130_AUX.SchDoc		Size: B	
Engineer: Xingqi (Eric) Chen		Contact: http://www.ti.com/support			

